

Film Capacitors

Metallized Polypropylene Film Capacitors (MKP)

Series/Type: B32674 ... B32678

Date: June 2013

Typical applications

For high ripple current modules in:

- Frequency converters
- Industrial and high-end power supplies
- Solar inverters

Climatic

- Max. operating temperature: 105 °C (case)
- Climatic category (IEC 60068-1): 40/100/56

Construction

- Dielectric: metallized polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

Features

- Excellent self-healing properties
- Over-voltage capability
- Optimized electrical contact
- High frequency ripple current
- High reliability
- Long useful life
- RoHS-compatible
- Halogen-free capacitors available on request

Terminals

- Parallel wire leads, lead-free tinned
- 2-pin and 4-pin versions
- Standard lead lengths: 6 – 1 mm
- Special lead lengths are available on request

Marking

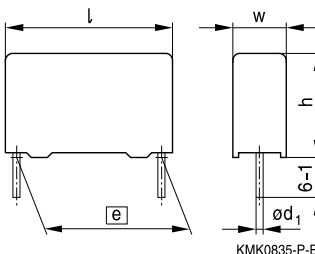
Manufacturer's logo and lot number, date code, rated capacitance (coded), capacitance tolerance (code letter), rated DC voltage

Delivery mode

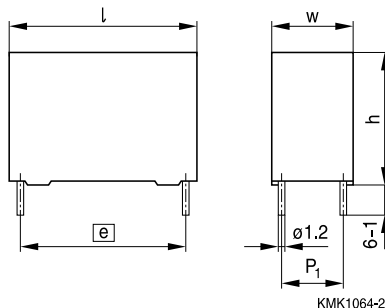
Bulk (untaped, lead length 6 – 1 mm)

Dimensional drawings

2-pin version



4-pin version



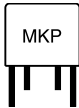
Dimensions in mm

Version	Lead spacing $e \pm 0.4$	Lead diameter d_1	Type
2-pin	27.5	0.8	B32674D
2-pin	37.5	1.0	B32676T
4-pin	37.5	1.2	B32676G
4-pin	52.5	1.2	B32678G



Overview of available types

Lead spacing	27.5 mm				
Type	B32674				
Page	6				
V _R (V DC)	300	450	630	750	875
C _R (μF)					
0.47					
0.68					
1.0					
1.5					
2.2					
3.0					
3.3					
4.0					
4.7					
5.0					
5.6					
6.0					
6.8					
7.5					
8.0					
8.2					
10					
12					



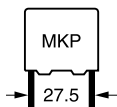
B32674 ... B32678

MKP DC link – high power series

Lead spacing	37.5 mm				
Type	B32676				
Page	8				
V _R (V DC)	300	450	630	750	875
C _R (μF)					
1.5					
2.0					
2.7					
3.3					
3.5					
4.0					
4.7					
5.6					
6.2					
6.8					
7.5					
8.2					
9.0					
10					
12					
14					
15					
20					
22					
25					
30					
35					



Lead spacing	52.5 mm				
Type	B32678				
Page	10				
V_R (V DC)	300	450	630	750	875
C_R (μF)					
15					
20					
25					
30					
35					
40					
47					
60					



B32674

MKP DC link – high power series

Ordering codes and packing units (lead spacing 27.5 mm)

C_R	Max. dimensions $w \times h \times l$	P_1	Ordering code (composition see below)	$I_{RMS,max}$ 70 °C 10 kHz A	$I_{RMS,max}$ 70 °C 20 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	Straight terminals, Untaped pcs./MOQ
μF	mm	mm					
$V_{R,85\text{ °C}} = 300\text{ V DC}, V_{op,70\text{ °C}} = 450\text{ V DC}$							
2.2	11.0 × 19.0 × 31.5	—	B32674D3225+000	7.5	6.5	7.5	1280
3.3	12.5 × 21.5 × 31.5	—	B32674D3335+000	9.0	8.0	5.3	1120
4.7	14.0 × 24.5 × 31.5	—	B32674D3475+000	11.0	9.5	4.1	1040
5.0	15.0 × 24.5 × 31.5	—	B32674D3505+000	11.0	10.0	4.0	960
5.6	15.0 × 24.5 × 31.5	—	B32674D3565+000	11.5	10.5	3.9	960
6.8	18.0 × 27.5 × 31.5	—	B32674D3685+000	12.5	11.5	3.7	800
8.0	16.0 × 32.0 × 31.5	—	B32674D3805+000	13.5	12.5	3.4	880
8.2	18.0 × 33.0 × 31.5	—	B32674D3825+000	14.0	13.0	3.2	800
10	21.0 × 31.0 × 31.5	—	B32674D3106+000	15.0	14.0	2.9	720
12	22.0 × 36.5 × 31.5	—	B32674D3126+000	16.0	15.0	2.5	640
$V_{R,85\text{ °C}} = 450\text{ V DC}, V_{op,70\text{ °C}} = 630\text{ V DC}$							
1.5	11.0 × 19.0 × 31.5	—	B32674D4155+000	6.5	6.0	9.2	1280
2.2	12.5 × 21.5 × 31.5	—	B32674D4225+000	8.0	7.0	7.8	1120
3.3	15.0 × 24.5 × 31.5	—	B32674D4335+000	9.5	8.5	5.2	960
4.7	18.0 × 27.5 × 31.5	—	B32674D4475+000	11.5	10.5	5.0	800
5.0	16.0 × 32.0 × 31.5	—	B32674D4505+000	12.0	11.0	5.0	880
5.6	18.0 × 33.0 × 31.5	—	B32674D4565+000	12.5	11.0	4.4	800
6.0	21.0 × 31.0 × 31.5	—	B32674D4605+000	13.0	11.5	4.3	720
6.8	22.0 × 36.5 × 31.5	—	B32674D4685+000	14.0	12.5	4.0	640
7.5	22.0 × 36.5 × 31.5	—	B32674D4755+000	15.0	13.0	3.9	640
$V_{R,85\text{ °C}} = 630\text{ V DC}, V_{op,70\text{ °C}} = 800\text{ V DC}$							
1.0	11.0 × 19.0 × 31.5	—	B32674D6105+000	5.0	4.5	14.4	1280
1.5	12.5 × 21.5 × 31.5	—	B32674D6155+000	6.5	6.0	14.3	1120
2.2	15.0 × 24.5 × 31.5	—	B32674D6225+000	7.0	6.0	8.0	960
3.3	16.0 × 32.0 × 31.5	—	B32674D6335+000	7.0	6.0	6.5	880
4.7	22.0 × 36.5 × 31.5	—	B32674D6475+000	9.5	9.5	5.8	640
5.0	22.0 × 36.5 × 31.5	—	B32674D6505+000	10.5	9.5	5.8	640

MOQ = Minimum Order Quantity, consisting of 4 packing units.

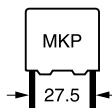
Further E series and intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

K = ±10%

J = ±5%

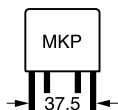
B32674
MKP DC link – high power series

Ordering codes and packing units (lead spacing 27.5 mm)

C_R	Max. dimensions $w \times h \times l$	P_1	Ordering code (composition see below)	$I_{RMS,max}$ 70 °C 10 kHz A	$I_{RMS,max}$ 70 °C 20 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	Straight terminals, Untaped pcs./MOQ
μF	mm	mm					
$V_{R,85\text{ °C}} = 750\text{ V DC}, V_{op,70\text{ °C}} = 900\text{ V DC}$							
0.68	11.0 × 19.0 × 31.5	—	B32674D1684+000	5.0	4.5	23.3	1280
1.0	12.5 × 21.5 × 31.5	—	B32674D1105+000	6.0	5.5	12.4	1120
1.5	14.0 × 24.5 × 31.5	—	B32674D1155+000	7.5	6.0	9.5	1040
2.2	18.0 × 27.5 × 31.5	—	B32674D1225+000	9.0	7.5	6.6	800
3.3	21.0 × 31.0 × 31.5	—	B32674D1335+000	10.0	9.0	6.0	720
4.0	22.0 × 36.5 × 31.5	—	B32674D1405+000	11.0	10.0	5.6	640
$V_{R,85\text{ °C}} = 875\text{ V DC}, V_{op,70\text{ °C}} = 1050\text{ V DC}$							
0.47	11.0 × 19.0 × 31.5	—	B32674D8474+000	5.0	4.5	22.9	1280
0.68	11.0 × 21.0 × 31.5	—	B32674D8684+000	6.0	5.5	18.6	1280
1.0	13.5 × 23.0 × 31.5	—	B32674D8105+000	7.5	6.0	13.6	1040
1.5	18.0 × 27.5 × 31.5	—	B32674D8155+000	7.0	6.5	8.5	800
2.2	18.0 × 33.0 × 31.5	—	B32674D8225+000	10.0	9.0	5.1	800
3.0	22.0 × 36.5 × 31.5	—	B32674D8305+000	11.0	10.0	6.8	640

MOQ = Minimum Order Quantity, consisting of 4 packing units.
Further E series and intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:
K = ±10%
J = ±5%



B32676

MKP DC link – high power series

Ordering codes and packing units (lead spacing 37.5 mm)

C_R	Max. dimensions $w \times h \times l$	P_1	Ordering code (composition see below)	$I_{RMS,max}$ 70 °C 10 kHz A	$I_{RMS,max}$ 70 °C 20 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	Straight terminals, Untaped pcs./MOQ
μF	mm	mm					
$V_{R,85\text{ °C}} = 300\text{ V DC}, V_{op,70\text{ °C}} = 450\text{ V DC}$							
6.2	24.0 × 15.0 × 41.5	—	B32676T3625+000	9.5	9.5	6.0	1040
9	24.0 × 19.0 × 41.5	—	B32676T3905K000	11.0	10.5	4.5	780
15	20.0 × 39.5 × 42.0	10.2	B32676G3156+000	15.0	14.0	3.2	640
20	28.0 × 37.0 × 42.0	10.2	B32676G3206+000	15.5	14.5	2.7	440
22	28.0 × 42.5 × 42.0	10.2	B32676G3226+000	16.0	15.0	2.6	440
25	28.0 × 42.5 × 42.0	10.2	B32676G3256+000	16.5	15.5	2.4	440
30	30.0 × 45.0 × 42.0	20.3	B32676G3306+000	17.0	16.0	2.3	400
35	33.0 × 48.0 × 42.0	20.3	B32676G3356+000	18.0	17.0	1.5	192
$V_{R,85\text{ °C}} = 450\text{ V DC}, V_{op,70\text{ °C}} = 630\text{ V DC}$							
4.0	24.0 × 15.0 × 41.5	—	B32676T4405+000	8.5	8.5	7.0	1040
4.7	24.0 × 9.0 × 41.5	—	B32676T4475+000	9.5	9.0	7.0	780
8.2	20.0 × 39.5 × 42.0	10.2	B32676G4825+000	11.5	10.0	8.8	640
10	20.0 × 39.5 × 42.0	10.2	B32676G4106+000	12.5	11.0	7.3	640
15	28.0 × 42.5 × 42.0	10.2	B32676G4156+000	14.0	13.0	5.0	440
20	30.0 × 45.0 × 42.0	20.3	B32676G4206K000	16.0	15.0	4.0	400
25	33.0 × 48.0 × 42.0	20.3	B32676G4256K000	18.0	17.0	1.5	192
$V_{R,85\text{ °C}} = 630\text{ V DC}, V_{op,70\text{ °C}} = 800\text{ V DC}$							
2.7	24.0 × 15.0 × 41.5	—	B32676T6275+000	8.0	8.0	8.0	1040
3.5	24.0 × 19.0 × 41.5	—	B32676T6355+000	9.0	9.0	7.0	780
6.8	20.0 × 39.5 × 42.0	10.2	B32676G6685+000	10.0	9.5	7.1	640
7.5	20.0 × 39.5 × 42.0	10.2	B32676G6755+000	10.0	9.5	6.7	640
8.2	28.0 × 37.0 × 42.0	10.2	B32676G6825+000	10.5	10.0	6.2	440
10	28.0 × 42.5 × 42.0	10.2	B32676G6106+000	11.0	10.5	5.7	440
12	28.0 × 42.5 × 42.0	10.2	B32676G6126+000	11.5	11.0	5.5	440
14	30.0 × 45.0 × 42.0	20.3	B32676G6146+000	12.0	11.5	3.6	400
15	33.0 × 48.0 × 42.0	20.3	B32676G6156+000	15.0	14.0	2.3	192

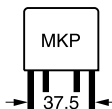
MOQ = Minimum Order Quantity, consisting of 4 packing units.
Further E series and intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

K = ±10%

J = ±5%


Ordering codes and packing units (lead spacing 37.5 mm)

C_R	Max. dimensions $w \times h \times l$	P_1	Ordering code (composition see below)	$I_{RMS,max}$ 70 °C 10 kHz A	$I_{RMS,max}$ 70 °C 20 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	Straight terminals, Untaped pcs./MOQ
μF	mm	mm					
$V_{R,85\text{ °C}} = 750\text{ V DC}, V_{op,70\text{ °C}} = 900\text{ V DC}$							
2.0	24.0 × 15.0 × 41.5	—	B32676T1205+000	7.5	7.5	10.0	1040
2.7	24.0 × 19.0 × 41.5	—	B32676T1275+000	8.5	8.5	8.0	780
4.7	20.0 × 39.5 × 42.0	10.2	B32676G1475+000	11.0	10.0	7.8	640
5.6	20.0 × 39.5 × 42.0	10.2	B32676G1565+000	11.5	10.5	7.1	640
6.8	28.0 × 37.0 × 42.0	10.2	B32676G1685+000	12.5	11.5	6.7	440
9.0	30.0 × 45.0 × 42.0	20.3	B32676G1905+000	14.0	13.0	6.0	440
10	30.0 × 45.0 × 42.0	20.3	B32676G1106+000	15.0	14.0	5.8	400
12	33.0 × 48.0 × 42.0	20.3	B32676G1126+000	19.5	19.0	2.7	192
$V_{R,85\text{ °C}} = 875\text{ V DC}, V_{op,70\text{ °C}} = 1050\text{ V DC}$							
1.5	24.0 × 15.0 × 41.5	—	B32676T8155+000	7.0	7.0	12.0	1040
2.0	24.0 × 19.0 × 41.5	—	B32676T8205+000	8.0	7.5	9.0	780
3.3	20.0 × 39.5 × 42.0	10.2	B32676G8335+000	11.0	10.0	11.0	640
4.0	20.0 × 39.5 × 42.0	10.2	B32676G8405+000	11.5	10.5	9.8	640
4.7	28.0 × 37.0 × 42.0	10.2	B32676G8475+000	12.5	11.5	8.6	440
6.8	28.0 × 42.5 × 42.0	10.2	B32676G8685+000	14.0	13.0	8.3	440
7.5	30.0 × 45.0 × 42.0	20.3	B32676G8755+000	15.0	14.0	8.0	400
10	33.0 × 48.0 × 42.0	20.3	B32676G8106K000	19.5	19.0	3.7	192

MOQ = Minimum Order Quantity, consisting of 4 packing units.

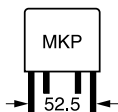
Further E series and intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

K = ±10%

J = ±5%



B32678

MKP DC link – high power series

Ordering codes and packing units (lead spacing 52.5 mm)

C_R	Max. dimensions $w \times h \times l$	P_1	Ordering code (composition see below)	$I_{RMS,max}$ 70 °C 10 kHz A	$I_{RMS,max}$ 70 °C 20 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	Straight terminals, Untaped pcs./MOQ
μF	mm	mm					
$V_{R,85\text{ °C}} = 300\text{ V DC}, V_{op,70\text{ °C}} = 450\text{ V DC}$							
40	$30.0 \times 45.0 \times 57.5$	20.3	B32678G3406+000	19.0	18.0	1.9	280
47	$35.0 \times 50.0 \times 57.5$	20.3	B32678G3476+000	21.0	20.0	1.7	108
60	$35.0 \times 50.0 \times 57.5$	20.3	B32678G3606K000	23.0	22.0	1.6	108
$V_{R,85\text{ °C}} = 450\text{ V DC}, V_{op,70\text{ °C}} = 630\text{ V DC}$							
30	$35.0 \times 50.0 \times 57.5$	20.3	B32678G4306+000	19.5	18.5	2.1	108
35	$35.0 \times 50.0 \times 57.5$	20.3	B32678G4356+000	21.0	20.0	1.7	108
40	$35.0 \times 50.0 \times 57.5$	20.3	B32678G4406K000	22.0	21.0	1.5	108
$V_{R,85\text{ °C}} = 630\text{ V DC}, V_{op,70\text{ °C}} = 800\text{ V DC}$							
20	$35.0 \times 50.0 \times 57.5$	20.3	B32678G6206+000	17.5	16.5	2.9	108
25	$35.0 \times 50.0 \times 57.5$	20.3	B32678G6256+000	20.0	19.0	2.6	108
$V_{R,85\text{ °C}} = 750\text{ V DC}, V_{op,70\text{ °C}} = 900\text{ V DC}$							
15	$30.0 \times 45.0 \times 57.5$	20.3	B32678G1156K000	20.0	19.5	3.7	280
20	$35.0 \times 50.0 \times 57.5$	20.3	B32678G1206K000	21.0	20.0	2.6	108
$V_{R,85\text{ °C}} = 875\text{ V DC}, V_{op,70\text{ °C}} = 1050\text{ V DC}$							
15	$35.0 \times 50.0 \times 57.5$	20.3	B32678G8156K000	21.0	20.0	3.4	108

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further E series and intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

K = $\pm 10\%$

J = $\pm 5\%$



Technical data

Reference standard: IEC 61071. All data given at T = 20 °C, unless otherwise specified.

Operating temperature range (case)		Max. operating temperature, T _{op,max} +105 °C Upper category temperature T _{max} +100 °C Lower category temperature T _{min} −40 °C				
ESR (at 10 kHz)	LS 27.5	< 3.0 · ESR _{typ}				
	LS 37.5	< 2.5 · ESR _{typ}				
	LS 52.5	< 1.5 · ESR _{typ}				
Insulation Resistance R _{ins} given as time constant t= C _R · R _{ins} , rel. humidity ≤ 65% (minimum as-delivered values)		30 000 s				
DC test voltage between terminals (10 s)		1.5 · V _R				
DC test voltage terminal to case (10 s)		2110 V AC, 50 Hz				
Maximum peak current (A)		I _{P,max} = C _R · $\frac{dV}{dt}$				
Damp heat test		56 days/40 °C/93% relative humidity				
Limit values after damp heat test		Capacitance change ΔC/C ≤ 5% Dissipation factor change Δ tan δ ≤ 1.5 · 10 ^{−3} (at 1 kHz) Insulation resistance R _{ins} ≥ 50% of minimum as-delivered values				
Reliability:	Failure rate λ	1 fit (≤ 1 · 10 ^{−9} /h) at 0.5 · V _R , 40 °C				
	Service life t _{SL}	200 000 h at V _R , 85 °C For conversion to other operating conditions and temperatures, refer to chapter "Quality, 2 Reliability".				
V _R (V DC)		300	450	630	750	875
Continuous operation voltage V _{op} (V DC) at 70 °C		450	630	800	900	1050
Continuous operation voltage V _{op} (V DC) at 85 °C		300	450	630	750	875
Maximum peak voltage V _{P,max} (V DC)		450	675	950	1125	1300
For temperatures between 85 °C and 100 °C		1.2%/°C of derating respect V _{op} at 85 °C				



B32674 ... B32678

MKP DC link – high power series

Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in V/μs.

Note:

The values of dV/dt and k_o provided below must not be exceeded in order to avoid damaging the capacitor. These parameters are given for isolated pulses in such a way that the heat generated by one pulse will be completely dissipated before applying the next pulse. For a train of pulses, please refer to the curves of permissible AC voltage-current versus frequency.

dV/dt values

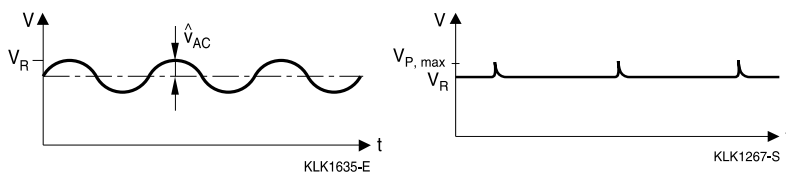
Lead spacing	27.5 mm					37.5 mm					52.5 mm				
Type	B32674					B32676					B32678				
V_R (V DC)	300	450	630	750	875	300	450	630	750	875	300	450	630	750	875
dV/dt in V/μs	40	75	100	125	150	22	54	73	85	100	15	35	50	60	70



ESL values

	ESL
2-pin	25 nH
4-pin	17 nH

Typical waveforms



Restrictions:

V_R : Maximum operating peak voltage of either polarity but of a non-reversing waveform, for which the capacitor has been designed for continuous operation.

$$\hat{V}_{AC} \leq 0.2 \cdot V_R$$

$V_{P, \max}$: Maximum permissible recurrent voltage that may appear for 2% of the period.



B32674 ... B32678

MKP DC link – high power series

Mounting guidelines

1 Soldering

1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

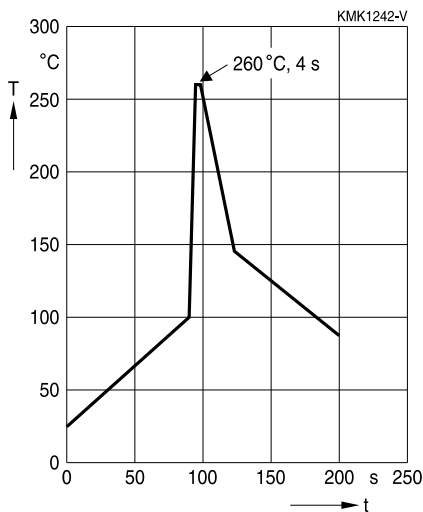
Solder bath temperature	235 ±5 °C
Soldering time	2.0 ±0.5 s
Immersion depth	2.0 +0/–0.5 mm from capacitor body or seating plane
Evaluation criteria:	
Visual inspection	Wetting of wire surface by new solder ≥90%, free-flowing solder

1.2 Resistance to soldering heat

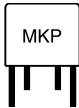
Resistance to soldering heat is tested to IEC 60068-2-20, test Tb, method 1A.

Conditions:

Series	Solder bath temperature	Soldering time
MKT boxed (except 2.5 × 6.5 × 7.2 mm) coated uncoated (lead spacing > 10 mm)	260 ±5 °C	10 ±1 s
MFP		
MKP (lead spacing > 7.5 mm)		
MKT boxed (case 2.5 × 6.5 × 7.2 mm)		5 ±1 s
MKP (lead spacing ≤ 7.5 mm)		< 4 s
MKT uncoated (lead spacing ≤ 10 mm) insulated (B32559)		recommended soldering profile for MKT uncoated (lead spacing ≤ 10 mm) and insulated (B32559)



Immersion depth	2.0 +0/−0.5 mm from capacitor body or seating plane
Shield	Heat-absorbing board, (1.5 ±0.5) mm thick, between capacitor body and liquid solder
Evaluation criteria:	
Visual inspection	No visible damage
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression capacitors
$\tan \delta$	As specified in sectional specification



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1.3 General notes on soldering

Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature $T_{\max}$. Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics:
diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

EPCOS recommends the following conditions:

- Pre-heating with a maximum temperature of 110 °C
- Temperature inside the capacitor should not exceed the following limits:
 - MKP/MFP 110 °C
 - MKT 160 °C
- When SMD components are used together with leaded ones, the leaded film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.
- Leaded film capacitors are not suitable for reflow soldering.

Uncoated capacitors

For uncoated MKT capacitors with lead spacings ≤ 10 mm (B32560/B32561) the following measures are recommended:

- pre-heating to not more than 110 °C in the preheater phase
- rapid cooling after soldering

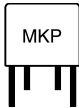


Cautions and warnings

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Topic	Safety information	Reference chapter "General technical information"
Storage conditions	Make sure that capacitors are stored within the specified range of time, temperature and humidity conditions.	4.5 "Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive flammability), avoid overload of the capacitors (active flammability) and consider the flammability of materials.	5.3 "Flammability"
Resistance to vibration	Do not exceed the tested ability to withstand vibration. The capacitors are tested to IEC 60068-2-6. EPCOS offers film capacitors specially designed for operation under more severe vibration regimes such as those found in automotive applications. Consult our catalog "Film Capacitors for Automotive Electronics".	5.2 "Resistance to vibration"



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Topic	Safety information	Reference chapter "Mounting guidelines"
Soldering	Do not exceed the specified time or temperature limits during soldering.	1 "Soldering"
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"
Embedding of capacitors in finished assemblies	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account. Caution: Consult us first, if you also wish to embed other uncoated component types!	3 "Embedding of capacitors in finished assemblies"



Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
α_C	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
A	Capacitor surface area	Kondensatoroberfläche
β_C	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
C	Capacitance	Kapazität
C_R	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative deviation of actual value)	Relative Kapazitätsänderung (relative Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation from rated capacitance)	Kapazitätstoleranz (relative Abweichung vom Nennwert)
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change (self-heating)	Absolute Temperaturänderung (Selbsterwärmung)
$\Delta \tan \delta$	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate of voltage rise)	Differentielle Spannungsänderung (Spannungsflankensteilheit)
$\Delta V/\Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f_1	Frequency limit for reducing permissible AC voltage due to thermal limits	Grenzfrequenz für thermisch bedingte Reduzierung der zulässigen Wechselspannung
f_2	Frequency limit for reducing permissible AC voltage due to current limit	Grenzfrequenz für strombedingte Reduzierung der zulässigen Wechselspannung
f_r	Resonant frequency	Resonanzfrequenz
F_D	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
F_T	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I_C	Category current (max. continuous current)	Kategoriestrom (max. Dauerstrom)


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Symbol	English	German
I_{RMS}	(Sinusoidal) alternating current, root-mean-square value	(Sinusförmiger) Wechselstrom
i_z	Capacitance drift	Inkonstanz der Kapazität
k_0	Pulse characteristic	Impulskennwert
L_S	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λ_0	Constant failure rate during useful service life	Konstante Ausfallrate in der Nutzungsphase
λ_{test}	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P_{diss}	Dissipated power	Abgegebene Verlustleistung
P_{gen}	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des Entladekreises
R_i	Internal resistance	Innenwiderstand
R_{ins}	Insulation resistance	Isolationswiderstand
R_P	Parallel resistance	Parallelwiderstand
R_S	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtest)
t	Time	Zeit
T	Temperature	Temperatur
τ	Time constant	Zeitkonstante
$\tan \delta$	Dissipation factor	Verlustfaktor
$\tan \delta_D$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
$\tan \delta_P$	Parallel component of dissipation factor	Parallelanteil des Verlustfaktors
$\tan \delta_S$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T_A	Ambient temperature	Umgebungstemperatur
T_{max}	Upper category temperature	Obere Kategorietemperatur
T_{min}	Lower category temperature	Untere Kategorietemperatur
t_{OL}	Operating life at operating temperature and voltage	Betriebszeit bei Betriebstemperatur und -spannung
T_{op}	Operating temperature	Betriebstemperatur
T_R	Rated temperature	Nenntemperatur
T_{ref}	Reference temperature	Referenztemperatur
t_{SL}	Reference service life	Referenz-Lebensdauer
V_{AC}	AC voltage	Wechselspannung



Symbol	English	German
V_C	Category voltage	Kategoriespannung
$V_{C,RMS}$	Category AC voltage	(Sinusförmige) Kategorie-Wechselspannung
V_{CD}	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
V_{ch}	Charging voltage	Ladespannung
V_{DC}	DC voltage	Gleichspannung
V_{FB}	Fly-back capacitor voltage	Spannung (Flyback)
V_i	Input voltage	Eingangsspannung
V_o	Output voltage	Ausgangssspannung
V_{op}	Operating voltage	Betriebsspannung
V_p	Peak pulse voltage	Impuls-Spitzenspannung
V_{pp}	Peak-to-peak voltage Impedance	Spannungshub
V_R	Rated voltage	Nennspannung
$\hat{V}_R$	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
V_{RMS}	(Sinusoidal) alternating voltage, root-mean-square value	(Sinusförmige) Wechselspannung
V_{SC}	S-correction voltage	Spannung bei Anwendung "S-correction"
V_{sn}	Snubber capacitor voltage	Spannung bei Anwendung "Beschaltung"
Z	Impedance	Scheinwiderstand
e	Lead spacing	Rastermaß

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
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